

# Search History

| Ref # | Hits | Search Query   | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|--|---|------------------|---------|------------------|
| L1    | 1    | 10/737776  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/08/08 09:36 |
| L2    | 4    | ((("5357621") or ("6646908")).PN.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/08/08 09:36 |
| L4    | 54   | (memory same module) and<br>substrate and terminal\$1 and<br>address and command and<br>(plurality same (memory with<br>component\$1)) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/08/08 09:52 |
| L5    | 20   | 4 and (access same circuit) and<br>input and output  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/08/08 09:45 |
| L6    | 0    | 5 and (address same signal) and<br>(generate%2 same outside)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/08/08 09:46 |
| L7    | 12   | 5 and (address same signal) and<br>(column same address) and (row<br>same address)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/08/08 09:46 |
| L8    | 8    | 7 and (bit same line) and (word<br>same line)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/08/08 09:47 |
| L9    | 8    | 8 and (selected same component)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/08/08 09:49 |

|     |    |  |   |    |    |                  |
|-----|----|--|---|----|----|------------------|
| L10 | 3  | 9 and "365"/\$7.ccls.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/08/08 09:48 |
| L11 | 1  | 4 and jakobs.in.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/08/08 09:48 |
| L12 | 18 | 4 and "365"/\$7.ccls.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/08/08 09:53 |
| L13 | 1  | ((carrier near\$2 substrate) and terminal\$1 and address and command and (plurailty near\$3 component\$1) and (access near\$2 circuit) and (input adj side) and (output adj side) and (column adj address) and (row adj address) and (bit adj line) and (word adj line) and (transmit\$4 or transfer\$4)).clm. | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/08/08 09:57 |